

In re the Patent of: Yasuo YAMAGISHI

U.S. Patent No. 7,102,367

Issued: September 5, 2006

Examiner: Jermele M. HOLLINGTON

Group Art Unit: 2829

Date: January 25, 2007

Serial No.: 10/621,445

Filed: **July 18, 2003** 

For. PROBE CARD AND TESTING METHOD OF SEMICONDUCTOR CHIP,

CAPACITOR AND MANUFACTURING METHOD

# PETITION TO CORRECT INVENTORSHIP IN PATENT **UNDER 37 CFR § 1.324**

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 Sir:

Applicant respectfully request that the following names be deleted as inventors of the above-referenced patent: Takashi SHIOGA, John David BENIECKI and Kazuaki KURIHARA.

The invention of the above named three (3) individuals is no longer being claimed due to the cancellation of claims in the application.

Attached herewith are the following: Consent of Assignee to Change of Inventorship, Statements of each currently-named inventor agreeing to the change of inventorship; and a check in the amount of \$130.00 for the petition fee.

In the event any additional fees are required in connection with this paper, please charge Deposit Account No. 01-2340.

Respectfully submitted,

ARMSTRONG, KRATZ, QUINTOS, HANSON & BROOKS, LLP

William L. Brooks

Attorney for Applicant 01/26/2007 HMARZII 000060077 10621445

Reg. No. 34,129

01 FC:1464

130.00 OP

WLB/ak Atty. Docket No. 030868 Suite 1000 1725 K Street, N.W. Washington, D.C. 20006 (202) 659-2930



In re the Patent of: Yasuo YAMAGISHI et al.

U.S. Patent No. 7,102,367

Group Art Unit: 2829

Issued: September 5, 2006

Examiner: Jermele M. HOLLINGTON

Serial No.: 10/621,445

Filed: July 18, 2003

For. PROBE CARD AND TESTING METHOD OF SEMICONDUCTOR CHIP,

CAPACITOR AND MANUFACTURING METHOD

### CONSENT OF ASSIGNEE TO CHANGE OF INVENTORSHIP

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

I, Michio Nakamua (name), General Manger, (title) of FUJITSU LIMITED, 1-1,

Kamikodanaka 4-chome, Nakahara-ku, Kawasaki-shi Kanagawa 211-8588, Japan, the sole Assignee of the above-referenced patent application, hereby agree and consent to the deletion of Takashi SHIOGA, John David BENIECKI and Kazuaki KURIHARA as inventors of U.S. Patent 7,102,367.

(Name) Michio Nakamura

(Title) General Manager, Patentdiv.

FUJITSU LIMITED

1-1, Kamikodanaka 4-chome Nakahara-ku, Kawasaki-shi Kanagawa 211-8588, Japan

WLB/ak Atty. Docket No. **030868** Suite 1000 1725 K Street, N.W. Washington, D.C. 20006 (202) 659-2930

23850



In re the Patent of: Yasuo YAMAGISHI et al.

U.S. Patent No. 7,102,367

Group Art Unit: 2829

Issued: September 5, 2006

Examiner: Jermele M. HOLLINGTON

Serial No.: 10/621,445

Confirmation No. 1112

Filed: July 18, 2003

For. PROBE CARD AND TESTING METHOD OF SEMICONDUCTOR CHIP,

CAPACITOR AND MANUFACTURING METHOD THEREFOR

### STATEMENT OF INVENTOR

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

I, Yasuo YAMAGISHI, hereby confirm that I am the sole inventor of the above-referenced patent, and I hereby agree and consent to the deletion of Takashi SHIOGA, John David BANIECKI and Kazuaki KURIHARA as inventors of U.S. Patent 7,102,367.

Yasuo YAMAGISHI

Jan. 09, 2007

WLB/ak Atty. Docket No. **030868** Suite 1000 1725 K Street, N.W. Washington, D.C. 20006 (202) 659-2930

23850

In re the Patent of: Yasuo YAMAGISHI et al.

U.S. Patent No. 7,102,367

Group Art Unit: 2829

Issued: September 5, 2006

Examiner: Jermele M. HOLLINGTON

Serial No.: 10/621,445

Confirmation No. 1112

Filed: July 18, 2003

For. PROBE CARD AND TESTING METHOD OF SEMICONDUCTOR CHIP,

CAPACITOR AND MANUFACTURING METHOD THEREFOR

### STATEMENT OF INVENTOR

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

I, Takeshi SHIOGA, hereby confirm that I am not an inventor of the above-referenced patent, and I hereby agree and consent to the deletion of Takashi SHIOGA, John David BANIECKI and Kazuaki KURIHARA as inventors of U.S. Patent 7,102,367.

Takeshi SHIOGA

. Date

WLB/ak Atty. Docket No. **030868** Suite 1000 1725 K Street, N.W. Washington, D.C. 20006 (202) 659-2930

23850



In re the Patent of: Yasuo YAMAGISHI et al.

U.S. Patent No. 7,102,367

Group Art Unit: 2829

Issued: September 5, 2006

Examiner: Jermele M. HOLLINGTON

Serial No.: 10/621,445

Confirmation No. 1112

Filed: July 18, 2003

For. PROBE CARD AND TESTING METHOD OF SEMICONDUCTOR CHIP,

CAPACITOR AND MANUFACTURING METHOD THEREFOR

## STATEMENT OF INVENTOR

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

I, John David BANIECKI, hereby confirm that I am not an inventor of the above-referenced patent application, and I hereby agree and consent to the deletion of Takashi SHIOGA, John David BANIECKI and Kazuaki KURIHARA as inventors of U.S. Patent 7,102,367.

John David BANIECKI

Data

WLB/ak

Atty. Docket No. **030868** Suite 1000 1725 K Street, N.W.

Washington, D.C. 20006

(202) 659-2930

Jan 4, 2007

23850



In re the Patent of: Yasuo YAMAGISHI et al.

U.S. Patent No. 7,102,367

Group Art Unit: 2829

Issued: September 5, 2006

Examiner: Jermele M. HOLLINGTON

Serial No.: 10/621,445

Confirmation No. 1112

Filed: July 18, 2003

For. PROBE CARD AND TESTING METHOD OF SEMICONDUCTOR CHIP,

CAPACITOR AND MANUFACTURING METHOD THEREFOR

### STATEMENT OF INVENTOR

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

I, Kazuaki KURIHARA, hereby confirm that I am not an inventor of the above-referenced patent application, and I hereby agree and consent to the deletion of Takashi SHIOGA, John David BANIECKI and Kazuaki KURIHARA as inventors of U.S. Patent 7,102,367.

Kazuaki KURIHARA

Dec. 28, 2006

WLB/ak Atty. Docket No. **030868** Suite 1000 1725 K Street, N.W. Washington, D.C. 20006 (202) 659-2930

23850